## Article

# A Reconfiguration Method for Extracting Maximum Power from Non-Uniform Aging Solar Panels 

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#### Abstract

Aging affects different photovoltaic (PV) modules in a PV array in a non-uniform way, thereby leading to non-uniform working conditions of the PV modules and resulting in variations in the power outputs of the PV array. In this paper, an algorithm is developed for optimising the electrical configuration of a PV array during the non-uniform aging processes amongst the PV modules. A new PV array reconfiguration method is proposed to maximize the power generation from non-uniformly aged PV arrays through rearrangements of the positions of the PV modules without having to replace the aged PV modules with new ones, thereby saving on maintenance costs. This reconfiguration strategy requires information about the electrical parameters of the PV modules in an array, so as to choose the optimal reconfiguration topology. In this algorithm, the PV modules are sorted iteratively in a hierarchy pattern to reduce the effect of mismatch due to the non-uniform aging processes amongst PV modules. Computer simulation and analysis have been carried out to evaluate the effectiveness of the proposed method for different sizes of non-uniform aged PV arrays $(4 \times 4,10 \times 10$, and $100 \times 10$ arrays $)$ with MATLAB. The results show an improvement in the power generation from a non-uniformly aged PV array and can be applied to any size of PV array.


Keywords: solar PV; MPPT; non-uniform aging; rearrangement

## 1. Introduction

With increasing investment in photovoltaic (PV) power generation as a way of promoting green energy, there is a need to increase its energy efficiency and cost effectiveness, so as to make it competitive with other energy sources. PV systems are gradually gaining acceptance, and energy consumption from renewable sources is expected to increase to $20 \%$ by 2020 in Europe, because of their wide range of applications in power generation, transportation, and mobile appliances [1].

In spite of its numerous advantages, PV technology faces several barriers, and these barriers have limited its wide deployment. The most important barrier is the regional cost differences amongst different markets. Between 2010 and 2017, the utility-scale total installed costs in the United States and Italy had reduced to about $52 \%$ and $79 \%$, respectively, while the global weighted average levelized cost of energy (LCOE) of utility-scale PV plants had dropped from USD $0.36 / \mathrm{kWh}$ to USD $0.10 / \mathrm{kWh}$ [2]. Only in some remote locations, where fuel shipping costs are very expensive, has the PV generation achieved cost parity with conventional energy. The other barrier is its dependence on weather conditions, thereby causing stability and availability problems for the power grid [3].

In 2017, Si-wafer based PV technology and multi-crystalline technology accounted for about $95 \%$ and $62 \%$ of the overall production of photovoltaic cells, respectively. Between 2010 and 2017, the efficiencies of wafer-based silicon and CdTe modules had increased from about $12 \%$ to $17 \%$ and $9 \%$ to $16 \%$, respectively [4]. The PV system sizes range from small to large systems, with capacities from a few kilowatts to hundreds of megawatts [5].

In many applications, such as building-integrated photovoltaic (BIPV) or solar power plants, the solar PV arrays are subjected to various faults and aging conditions. Usually, these PV panels operate outdoors, thereby being exposed to harsh environments (like snow, dirt, bird-drops, and so on) as well as production factors, resulting in non-uniform aging of the PV module of the array, which inevitably results in a reduction in the output power production.

The effect of non-uniform aging in a PV array means that the electrical characteristics of individual PV modules will differ. Therefore, there is a need to improve the power efficiency of aging PV modules, as there is huge cost involved in replacing them with new ones [6]. Furthermore, increasing the efficiency in PV plants so as to increase power generation is a key point, thereby increasing incomes, and consequently reducing the cost of power generation.

It is necessary to develop methods to investigate PV module degradation as well as to develop systems in order to monitor the electrical characteristics of PV modules over a long period, so as to increase the PV system's effective service period [6,7]. The PV module performance is characterized by its maximum output power, which is dependent on its short-circuit current and open-circuit voltage.

In addition, there are several factors, like dust accumulation, humidity, and air velocity, that affect the performance of the PV module. Mekhilefa et al. [8] presented a detailed review on the effect of various parameters on the performance of PV module. Apart from these factors, the aging of the PV cells also has a negative impact on the performance of PV module [9].

In non-uniform operating conditions (due to clouds, shadows, dirtiness, manufacturing tolerances, aging, and different orientation angles of modules of the PV field) nearly all of the Maximum Power Point Tracking (MPPT) techniques do not effectively operate, and they may frequently fail the tracking [10]. Furthermore, localised heating phenomena caused by faulty conditions such as partial shading, fabrication flaws, material imperfection, damages, and so on, can lead to a very fast aging of PV modules, which can potentially cause fatality failures [11]. Indeed, studies regarding field-aged PV generators have shown that reverse bias hotspots represent one of the main causes of PV module failures [12].

However, PV modules with the same brand and same ratings are not exactly identical because of manufacturing tolerances or defects, thereby causing power losses known as mismatch losses [13]. In fact, aging is one of the factors that causes mismatching among cells, while mismatching, on the other hand, leads to non-uniform aging, which is a common problem in PV systems [14].

Furthermore, there are different possible configurations of PV modules in a PV array, such as series parallel (SP), bridge link (BL), honey comb (HC), and total cross tied (TCT). However, the most exploited reconfigurable architectures of the PV modules are SP and TCT [1,15,16]. In an SP configuration, the PV modules are connected in series to form strings that are sufficient to provide the voltage required by the inverter, then, these strings are connected in parallel to form an array so as to increase the total current.

In the TCT configuration, the PV modules are first parallel tied so that the voltages are equal and the currents are summed up; a number of these rows of modules are then connected in series. Although the performance of different PV array configurations has been analysed by different researchers, the choice of configuration may differ. Some researchers have analysed only basic configurations (series and parallel), while the others have chosen only TCT [17].

The main challenge in the reconfiguration of PV arrays concerns the large amount of possibilities that must be evaluated in order to find the best solution. Such a problem has been addressed in the literature, using multiple approaches, as follows: a genetic algorithm (GA) solution for the computation of reconfiguration patterns in PV arrays was proposed by [13,18], which was shown to be superior for
sorting techniques and the Brute Force (BF) approach, respectively. Sanseverino, E.R et al. (2015) [1] utilized the Munkres algorithm in order to obtain the optimum configuration for which it is possible to balance and minimize the aging of the switches within the switching matrix. Some methods were developed on how to reconfigure solar cells, in order to improve the power output in shaded conditions [19-21]. However, refs. [20,21] focused mostly on how to build the arrays without proposing real-time executable control algorithms, thereby leading to an unrealistic number of sensors and switches that must use complex control algorithms to determine when turning the switch on or off. An adaptive reconfiguration of solar arrays was proposed, which required significantly fewer voltages or current sensors and switches [22] than proposed by refs. [20,21]. Y. Hu et al. (2017) proposed an offline reconfiguration strategy in order to improve the energy efficiency of aged PV systems by analysing the potential reorganization options of the PV modules and finding the maximum power point [6], while ref. [22], used a genetic algorithm to obtain the optimised maximum power output from non-uniform aged PV modules in an array. Papers [6,22] have been shown to be effective in solving PV rearrangement problems for both small scale and large scale PV arrays, but need to search all of the possible combinations of PV arrangements, thereby increasing the computational complexity and time. This paper attempts to solve these challenges by proposing a reconfiguration algorithm, which involves a hierarchical and iterative sorting of the PV modules, based on their aging status; this proposed algorithm does not need to access all of the possible configurations for a particular PV array (huge number) in order to reach its optimum configuration, and that makes it relatively fast. In other words, the optimal configuration with the proposed algorithm can be found in a short time, and in turn can be applied for implementation in real time. Additionally, the affected PV modules to be swapped are the only ones involved in the transition, while the rest remain in their original positions, therefore reducing the number of relays to be used for switching purposes.

Overall, this paper is organized as follows: Section 2 analyses the characteristics of a PV array under non-uniform aging conditions. Section 3 illustrates the developed reconfiguration scheme for a non-uniformly aged PV array. Section 4 is the simulation results carried out for $4 \times 4,10 \times 10$, and $100 \times 10$ PV arrays, and the results are also discussed. Section 5 concludes the paper and future works are recommended.

## 2. PV Modeling and Characteristics

An accurate and comprehensive model for PV modules helps to understand how current and voltage behave in a PV array under both uniform and non-uniform aging conditions.

### 2.1. Model of Good Quality PV Cells

The single-diode model is widely adopted for modeling PV sources. Figure 1 shows the commonly used single-diode model of a PV module. The electrical model of the PV module is given by the authors of [23].

$$
\begin{equation*}
I=I_{p h}-I_{s}\left[e^{\left(\frac{q\left(V+I R_{s}\right)}{N_{s} K T A}\right)}-1\right]-\frac{\left(V+I R_{s}\right)}{R_{s h}} \tag{1}
\end{equation*}
$$

where $q$ is the electron charge, $K$ is the Boltmann constant, $N_{s}$ is the number of series connected cells, $I_{S}$ is the diode saturation current (A), $A$ is the ideality factor, and $T$ is the module temperature (K). As shown in Figure 1, $I$ is the PV module output current (A), $I_{p h}$ is the photon current (A), $R_{s}$ is the series resistance ( $\Omega$ ), and $R_{\text {sh }}$ is the shunt resistance ( $\Omega$ ). The values of $q$ and $K$ are $1.6 \times 10^{-19} \mathrm{C}$ and $1.37 \times 10^{-23} \mathrm{~J} / \mathrm{K}$, respectively.

According to the authors of [10,24], for commercial solar cells, the shunt resistance, $R_{\text {sh }}$, is much greater than the forward resistance of the diode, and thus can be neglected for simplicity, and only the series resistance, $R_{s}$, will be considered. Therefore, Equation (1) is reduced to the following:

$$
\begin{equation*}
I=I_{p h}-I_{s}\left[e^{\left(\frac{q\left(V+I R_{s}\right)}{N_{s} K T A}\right)}-1\right] \tag{2}
\end{equation*}
$$

In this research, the Solarex MSX60 PV module technical data sheet was used [23] and the aging factors used for simulation and illustration purposes were randomly generated. Each MSX60 PV module consists of 36 series connected polycrystalline cells, while the ideality, $A$, has been chosen as 1.2. The technical specifications of the Solarex MSX60 PV module are given in Table 1.


Figure 1. Single diode model.
Table 1. The key specifications of the Solarex MSX60 photovoltaic (PV) module [23].

| At Temperature $\boldsymbol{T}=\mathbf{2 5}{ }^{\circ} \mathbf{C}$ |  |  |
| :---: | :---: | :---: |
| Open circuit voltage | $V_{O C}$ | 21.0 V |
| Short circuit current | $I_{S C}$ | 3.74 A |
| Voltage at max. power | $V_{m}$ | 17.1 V |
| Current at max. power | $I_{m}$ | 3.5 A |
| Maximum power | $P_{m}$ | 59.9 W |

PV manufacturers typically provide the values of the open-circuit voltage ( $V_{O C}$ ), short-circuit current $\left(I_{S C}\right)$, and the maximum power point $\left(V_{M P}, I_{M P}\right)$ at standard test conditions (STC).

- Short-circuit current is the maximum current that a PV cell can generate.
- Open-circuit voltage is the maximum voltage across a PV cell.
- Maximum power point (MPP) is the point on the I-V (voltage-current) characteristic curve where the product of voltage, $V_{M P}$, and current, $\mathrm{I}_{\mathrm{MP}}$, is the maximum [25].

In addition, a model proposed by the authors of [26] was used to study and evaluate the I-V and $\mathrm{P}-\mathrm{V}$ characteristics of a PV array under non-uniform aging conditions.

### 2.2. Mismatch Analysis Due to Non-Uniform Aging

According to the authors of [6], the short circuit current varies more than the open circuit voltage when a PV cell undergoes aging experiment, as a result of the $p-n$ junction characteristics of the cell. In this paper, we will use the short circuit current to evaluate the aging condition of the PV module, while keeping the open circuit voltage unchanged for different aging conditions. It is also assumed that all of the cell-units in the same PV module undergo uniform aging, so that the entire PV module can be characterised by a single maximum short-circuit of any of the cell-unit. In a PV array consisting of $m$ series-connected PV modules, the output current of the modules are equal to each other, and the output voltage is the sum of the total module voltages.

$$
\begin{gather*}
I_{\text {total }}=I_{\text {module 1 }}=I_{\text {module 2 }}=\cdots=I_{\text {module } m}  \tag{3}\\
V_{\text {total }}=V_{\text {module 1 }}+V_{\text {module 2 }}+\cdots+V_{\text {module } m}=\sum_{i=1}^{m} V_{\text {module } i} \tag{4}
\end{gather*}
$$

Equations (3) and (4) are true when the modules are identical. Under good quality conditions, the three modules act identically and the voltages of the three modules are equal to a value of 63 V , as shown in Figure 2. Also, as the PV modules are identical, the same short-circuit current flows through the series-connected PV modules, which is equal to 3.74 A (see Figure 2).


Figure 2. I-V (voltage-current) and P-V (power-voltage) characteristics for series connection of good quality modules under standard test conditions.

However, during the non-uniform aging processes, there is a mismatch in the short-circuit current of the individual PV module. Each PV module is connected in parallel to a bypass diode, so as to avoid the occurrence of a hot spot. Figure 3 illustrates a series combination of three modules with different aging scales.


Figure 3. Series-connection of photovoltaic (PV) modules.
Because of the non-uniform aging condition, multiple steps and multiple peaks of the PV power output are observed in Figure 4. This categorises the operation of the PV array into three different operation levels, and each peak corresponds to a particular level. Level 1 corresponds to the stage where module 1 is active, while module 2 and module 3 are being bypassed by the diodes. At this level, the current is between 0 and 3.42 A , and the corresponding voltage is $0-21 \mathrm{~V}$. Similarly, level 2 corresponds to the stage where module 1 and module 2 are active, while module 3 is being bypassed. The current for the un-bypassed series-connection is set by the current of the most aged PV module (in this case is the module 2). The current at level 2 is between 0 and 2.28 A , and the corresponding voltage is $0-42 \mathrm{~V}$. Level 3 corresponds to the stage where all of the modules are active, that is, none of the modules are bypassed. Similarly, the current for the un-bypassed series connection is set by the current of the most aged PV module (in this case is the module 3). The current at level 3 is between 0 and 1.44, and the corresponding voltage is $0-61.2 \mathrm{~V}$. Furthermore, there are multiple maximum power points showing from the knee points for the different levels in the I-V characteristic curve. These knee points correspond to certain currents and voltages that are used to obtain the maximum power points at different locations on the $\mathrm{P}-\mathrm{V}$ curve. The knee point at level 1 operates at 16.98 V and 3.2 A , corresponding to 54.4 W , the knee point at level 2 operates at 34.58 V and 2.21 A corresponding to 77.7 W , while the knee point at level 3 operates at 54.69 V and 1.12 A , corresponding to 61.1 W , as illustrated in the P-V curve of Figure 4 . The knee point at level 2 represents the global maximum power point (GMPP).

In the next section, the series-connected modules would form strings, which are connected in parallel to form an SP configuration with non-uniform aging conditions, so as to illustrate our proposed reconfiguration algorithm.


Figure 4. I-V and P-V characteristics for series connection under non-uniform aging condition.

## 3. PV Array Reconfiguration Scheme

In an $\mathrm{N} \times \mathrm{M} P V$ array, N is the parallel-connected strings and M is the number of series-connected PV modules, as shown in Figure 5. It is worth noting that the voltage where the GMPP of a PV array is located in the $\mathrm{P}-\mathrm{V}$ curve represents the number of active modules for a given string voltage. Therefore, the maximum power of the PV array is obtained by the product of the sum of all of the string currents and the string voltage of the active modules.


Figure 5. An $\mathrm{N} \times \mathrm{M}$ (parallel-connected strings $\times$ number of series-connected PV modules) (PV array (series parallel (SP) configuration).

A PV array consisting of 16 aged modules connected in a $4 \times 4$ SP configuration, shown in Figure 6, will be used to illustrate this concept.


Figure 6. A $4 \times 4$ SP configuration with non-uniform aging.

In Figure 6, there are four parallel-connected strings (rows) and four series-connected modules (columns), while the per-unit values denote the non-uniform aging status in the PV array, which has a direct relationship to their individual short-circuit currents.

## PV Reconfiguration Algorithm

Generally, the possible number of arrangements for an $N \times M \mathrm{PV}$ array is $\binom{N M}{M}\binom{N M-M}{M}\binom{N M-2 M}{M}\binom{N M-3 M}{M} \cdots\binom{2 M}{M}\binom{M}{M} / N!$. For the $4 \times 4$ PV array, there will be 2,627,625 possible arrangements, and calculating the maximum power for all of the possible PV module arrangements for a large PV array (say larger values of N and M ) becomes very difficult. The proposed reconfiguration algorithm is based on an iterative and hierarchical sorting of PV modules, in order to achieve optimum configuration within several iterative steps. In the proposed algorithm, the aging scale (coefficient) shall be used as the varying parameter, as it is directly related to the short-circuit current of each individual PV module. The short-circuit current in a healthy module is set as 1 p.u. under the standard testing condition (STC), which represents $1000 \mathrm{~W} / \mathrm{m}^{2}$ irradiance and $25^{\circ} \mathrm{C}$ module temperature. The digits in the array represent the different aging factors of the PV modules, which has a direct relationship to their individual short-circuit currents. Let's consider a $4 \times 4$ PV array configuration, shown in Figure 6.

The working parameter shall be the aging factor (AF), which has already been expressed as per unit (p.u.) value of the health status of the individual PV module.

The following parameters are defined in order to outline our proposed algorithm in six steps.
For $n=1,2,3, \ldots, N-1, N$, where $N$ is the number of strings in the PV array.
$\sum A F_{\text {string } n}=$ sum of aging factors in a series-connected string.
$M_{\min (n)}=$ minimum aging factor in a series-connected modules for string $n$.
$P_{\min }=$ position of PV module with minimum aging factor in a series-connected modules.
$M_{\max (n+1)}=$ maximum aging factor in a series-connected modules for string $n+1$.
$P_{\max }=$ position of PV module with maximum aging factor in a series-connected modules.
Step 1: Obtain the summation of AFs for each string, as follows:

$$
\begin{align*}
& \sum A F_{\text {string } 1}=0.8+0.7+0.9+0.5=2.9  \tag{5}\\
& \sum A F_{\text {string } 2}=0.6+0.4+0.9+0.7=2.6  \tag{6}\\
& \sum A F_{\text {string } 3}=0.8+0.5+0.6+0.5=2.4  \tag{7}\\
& \sum A F_{\text {string } 4}=0.8+0.6+0.5+0.4=2.3  \tag{8}\\
& \begin{array}{|l|l|l|l|l|}
\hline 0.8 \mathrm{pu} & 0.7 \mathrm{pu} & 0.9 \mathrm{pu} & 0.5 \mathrm{pu} \\
\hline 0.6 \mathrm{pu} & 0.4 \mathrm{pu} & 0.9 \mathrm{pu} & 0.7 \mathrm{pu} \\
\hline 0.8 \mathrm{pu} & 0.5 \mathrm{pu} & 0.6 \mathrm{pu} & 0.5 \mathrm{pu} \\
\hline 0.8 \mathrm{pu} & 0.6 \mathrm{pu} & 0.5 \mathrm{pu} & 0.4 \mathrm{pu} \\
\hline 2.9 \mathrm{pu} \\
\hline 2.6 \mathrm{pu} \\
\hline 2.4 \mathrm{pu} \\
\hline 2.3 \mathrm{pu} \\
\hline
\end{array}
\end{align*}
$$

Step 2: Arrange the total string level AFs in a descending order, in the case study.
Step 3: Identify $M_{\min (n)}$ and $M_{\max (n+1)}$ for $n=1$.


If $M_{\min (1)}<M_{\max (2)}$, then swap $P_{\min }$ with $P_{\max }$ and repeat Steps 1,2, and 3.

| 0.8 pu | 0.7 pu | 0.9 pu | 0.9 pu |
| :---: | :---: | :---: | :---: |
| 0.6 pu | 0.4 pu | 0.5 pu | 0.7 pu |
| 0.8 pu | 0.5 pu | 0.6 pu | 0.5 pu |
| 0.8 pu | 0.6 pu | 0.5 pu | 0.4 pu |



Step 4: Repeat Steps 1, 2, and 3 until $M_{\min (n)} \geq M_{\max (n+1)}$.

| 0.8 pu | 0.7 pu | 0.9 pu | 0.9 pu | 3.3 pu |  | 0.8 pu | 0.8 pu | 0.9 pu | 0.9 pu | 3.4 pu |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 0.8 pu | 0.5 pu | 0.6 pu | 0.5 pu | 2.4 pu |  | 0.7 pu | 0.5 pu | 0.6 pu | 0.5 pu | 2.3 pu |
| 0.8 pu | 0.6 pu | 0.5 pu | 0.4 pu | 2.3 pu |  | 0.8 pu | 0.6 pu | 0.5 pu | 0.4 pu | 2.3 pu |
| 0.6 pu | 0.4 pu | 0.5 pu | 0.7 pu | 2.2 pu |  | 0.6 pu | 0.4 pu | 0.5 pu | 0.7 pu | 2.2 pu |

Step 5: Identify $M_{\min (n)}$ and $M_{\max (n+1)}$ for $n=2$, swap the corresponding $P_{\min }$ with $P_{\max }$, and repeat steps $1,2,3$, and 4 .


Step 6: Repeat step 5 until the end $(\mathrm{N}-1)$.


From the last step, the array atthe right hand side (RHS) shows the optimum configuration. However, each of the configurations that arrived at each step were compared with the original arrangement, as shown in Figure 6. As illustrated for the $4 \times 4$ PV array, only six iterative steps were needed to arrive at the optimum configuration for a non-uniform aging condition. Furthermore, a MATLAB program is written to execute for a large PV array, and the optimized form depicts the arrangements for the optimum power output. Therefore, for the instance of a $4 \times 4 \mathrm{PV}$ array, the array "after" arrangement is the optimum configuration. Figure 7 shows the comparison between the PV array "before" and "after" arrangement.

| 0.8 pu | 0.7 pu | 0.9 pu | 0.5 pu |
| :---: | :---: | :---: | :---: |
| 0.6 pu | 0.4 pu | 0.9 pu | 0.7 pu |
| 0.8 pu | 0.5 pu | 0.6 pu | 0.5 pu |
| 0.8 pu | 0.6 pu | 0.5 pu | 0.4 pu |

before

| 0.8 pu | 0.8 pu | 0.9 pu | 0.9 pu |
| :---: | :---: | :---: | :---: |
| 0.7 pu | 0.8 pu | 0.6 pu | 0.7 pu |
| 0.6 pu | 0.6 pu | 0.5 pu | 0.5 pu |
| 0.5 pu | 0.4 pu | 0.5 pu | 0.4 pu |

after

Figure 7. PV Array "before" and "after" rearrangement.
The maximum power and voltage at MPP were tabulated in Table 2 for each configuration and the string currents in each case. Clearly, the total output power is increased by $22.4 \%$ between Step 1 and Step 6. Also, the voltage at MPP does not vary much compared to the output current. This algorithm maximizes the currents in each string by combining the PV modules with almost the same electrical characteristics, so as to reduce multiple peaks resulting from mismatch effects (non-uniform aging).

Table 2. Electrical parameters obtained for different reconfigurations. MPP—maximum power point.

| Steps | Maximum Power $\left(\boldsymbol{P}_{\boldsymbol{m}}\right)$ <br> $\mathbf{( W )}$ | Voltage at MPP $\left(\boldsymbol{V}_{\boldsymbol{m}}\right)$ <br> $\mathbf{( V )}$ | String Currents (A) |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | 71 | $\mathbf{1}$ | $\mathbf{2}$ | $\mathbf{3}$ | $\mathbf{4}$ |
| 1 | 515.6 | 70 | 2.863 | 1.492 | 1.757 | 1.482 |
| 2 | 530.7 | 70 | 2.818 | 1.793 | 1.490 | 1.488 |
| 3 | 535.3 | 70 | 2.818 | 1.859 | 1.490 | 1.488 |
| 4 | 558.6 | 70 | 2.818 | 2.206 | 1.475 | 1.475 |
| 5 | 573.7 | 69 | 2.867 | 2.260 | 1.790 | 1.428 |
| 6 |  |  |  |  |  |  |

To execute the MATLAB program. the PV array shall be represented in matrix form, given by Equation (11).

$$
\mathrm{N} \text { by } \mathrm{M}_{\text {matrix }}=\left[\begin{array}{cccc}
0.8 & 0.7 & 0.9 & 0.5  \tag{11}\\
0.6 & 0.4 & 0.9 & 0.7 \\
0.8 & 0.5 & 0.6 & 0.5 \\
0.8 & 0.6 & 0.5 & 0.4
\end{array}\right]
$$

The flowchart of the reconfiguration algorithm for an $\mathrm{N} \times \mathrm{M} \mathrm{PV}$ array is shown in Figure 5 . The objective of this proposed algorithm is to swap the positions of the individual PV modules in each string, based on their aging factors, so as to reduce the effect of the mismatch losses amongst the PV modules in a particular string.

The developed algorithm requires only instantaneous short-circuit current information for data entry (represented as aging factors, as aging is directly related to the short-circuit current). Furthermore, the algorithm continues until all of the criteria are met, as illustrated in Figure 8, in order to obtain the optimum configuration.


Figure 8. Flow chart of reconfiguration strategy.

## 4. Results and Discussions

### 4.1. Simulation Results

In order to validate the proposed algorithm, PV arrays of different sizes will be considered, that is, $4 \times 4,10 \times 10$, and $100 \times 10 \mathrm{PV}$ arrays. The maximum output powers from the PV configuration before and after arrangement are obtained using a PV array model built in MATLAB.

A computer with Intel (R) Core (TM) i3-5005U CPU at 2.00 GHz , with 4 G RAM was used to perform the calculation and the corresponding computing time for a $4 \times 4,10 \times 10$, and $100 \times 10 \mathrm{PV}$ array tabulated.

### 4.2. Case 1: $4 \times 4$ PV Array

Figure 6 will be used in this case to verify the results using MATLAB. The maximum short-circuit current in a healthy module is set as 1 p.u. under the standard testing condition (STC), which represents the $1000 \mathrm{~W} / \mathrm{m}^{2}$ irradiance and $25^{\circ} \mathrm{C}$ module temperature.

In Table 3, the PV configuration after arrangement was obtained using the proposed algorithm. With the information of the PV array shown in Table 3, the $I-V$ and $P-V$ characteristic curves are plotted as shown in Figure 9. Figure 9 shows that the maximum output power before arrangement, which is 468.6 W , and the PV array output voltage and current for the GMPP are 71 V and 6.498 A , respectively. The maximum output power after arrangement is 573.7 W , and the PV array output voltage and current for the GMPP are 69 V and 8.255 A , respectively. Obviously, the total output power increases by $22.4 \%$ when the proposed algorithm is used. The computing time for the rearrangements of an aged $4 \times 4 \mathrm{PV}$ array takes 0.01 s to execute our proposed algorithm as shown in Table 4 .

Table 3. PV configuration before and after arrangement for Case 1.

| Before Arrangement |  |  |  | After Arrangement |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| 0.8 pu | 0.7 pu | 0.9 pu | 0.5 pu | 0.8 pu | 0.8 pu | 0.9 pu | 0.9 pu |
| 0.6 pu | 0.4 pu | 0.9 pu | 0.7 pu | 0.7 pu | 0.8 pu | 0.6 pu | 0.7 pu |
| 0.8 pu | 0.5 pu | 0.6 pu | 0.5 pu | 0.6 pu | 0.6 pu | 0.5 pu | 0.5 pu |
| 0.8 pu | 0.6 pu | 0.5 pu | 0.4 pu | 0.5 pu | 0.4 pu | 0.5 pu | 0.4 pu |

Table $4.4 \times 4$ PV array parameters (before and after arrangement).

| Parameter | Before | After | Computing Time (s) | Power Improvement (Percentage) |
| :---: | :---: | :---: | :---: | :---: |
| $P_{m}$ | 468.6 W | 573.7 W |  |  |
| $V_{m}$ | 71 V | 69 V | 0.01 | $22.4 \%$ |
| $I_{m}$ | 6.498 A | 8.255 A |  |  |



Figure 9. The array output (before and after arrangement) for case 1.

### 4.3. Case 2: $10 \times 10$ PV Array

For a medium size $10 \times 10 \mathrm{PV}$ array, there are 10 parallel-connected strings and 10 series-connected modules. Here, the aging factors, ranging from 0.4 p.u. to 0.9 p.u., were randomly generated in MATLAB (R2015a) to form a $10 \times 10$ matrix, replicating a non-uniform aging PV array before arrangement, as shown in Table 5, as well as the optimum PV configuration (after arrangement) for this case. These two PV configurations were simulated and the result shows that the proposed algorithm gives an optimum configuration. Figure 10 shows that the maximum output power before arrangement is 2754 W , and the PV array output voltage and current for the GMPP are 164 V and 16.95 A, respectively. The maximum output power after arrangement is 3920 W , and the PV array output voltage and current for the GMPP are 171 V and 21.13 A , respectively. The computing time for the rearrangements of an aged $10 \times 10 \mathrm{PV}$ array takes 0.035 s to execute in our proposed algorithm as shown in Table 6.

Table 5. PV configuration before and after arrangement for Case 2.

| Before Arrangement |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 0.4 pu | 0.6 pu | 0.4 pu | 0.6 pu | 0.9 pu | 0.6 pu | 0.8 pu | 0.5 pu | 0.7 pu | 0.9 pu |
| 0.8 pu | 0.4 pu | 0.9 pu | 0.9 pu | 0.7 pu | 0.4 pu | 0.6 pu | 0.6 pu | 0.5 pu | 0.8 pu |
| 0.5 pu | 0.5 pu | 0.4 pu | 0.5 pu | 0.6 pu | 0.9 pu | 0.5 pu | 0.8 pu | 0.8 pu | 0.6 pu |
| 0.7 pu | 0.9 pu | 0.8 pu | 0.5 pu | 0.7 pu | 0.9 pu | 0.6 pu | 0.4 pu | 0.5 pu | 0.6 pu |
| 0.4 pu | 0.4 pu | 0.8 pu | 0.4 pu | 0.6 pu | 0.6 pu | 0.4 pu | 0.4 pu | 0.8 pu | 0.6 pu |
| 0.7 pu | 0.8 pu | 0.9 pu | 0.4 pu | 0.4 pu | 0.6 pu | 0.4 pu | 0.5 pu | 0.5 pu | 0.5 pu |
| 0.5 pu | 0.7 pu | 0.4 pu | 0.9 pu | 0.5 pu | 0.6 pu | 0.9 pu | 0.7 pu | 0.6 pu | 0.7 pu |
| 0.7 pu | 0.9 pu | 0.6 pu | 0.7 pu | 0.4 pu | 0.9 pu | 0.9 pu | 0.8 pu | 0.7 pu | 0.7 pu |
| 0.8 pu | 0.4 pu | 0.5 pu | 0.7 pu | 0.5 pu | 0.6 pu | 0.7 pu | 0.7 pu | 0.8 pu | 0.8 pu |
| 0.8 pu | 0.6 pu | 0.8 pu | 0.4 pu | 0.5 pu | 0.4 pu | 0.4 pu | 0.6 pu | 0.4 pu | 0.8 pu |
| After Arrangement |  |  |  |  |  |  |  |  |  |
| 0.9 pu | 0.9 pu | 0.9 pu | 0.9 pu | 0.9 pu | 0.9 pu | 0.9 pu | 0.9 pu | 0.9 pu | 0.9 pu |
| 0.9 pu | 0.8 pu | 0.8 pu | 0.8 pu | 0.9 pu | 0.9 pu | 0.8 pu | 0.8 pu | 0.8 pu | 0.8 pu |
| 0.8 pu | 0.8 pu | 0.8 pu | 0.8 pu | 0.8 pu | 0.8 pu | 0.8 pu | 0.8 pu | 0.7 pu | 0.8 pu |
| 0.7 pu | 0.7 pu | 0.7 pu | 0.7 pu | 0.7 pu | 0.7 pu | 0.7 pu | 0.7 pu | 0.7 pu | 0.7 pu |
| 0.7 pu | 0.6 pu | 0.6 pu | 0.6 pu | 0.7 pu | 0.6 pu | 0.6 pu | 0.7 pu | 0.7 pu | 0.6 pu |
| 0.6 pu | 0.6 pu | 0.6 pu | 0.6 pu | 0.6 pu | 0.6 pu | 0.6 pu | 0.6 pu | 0.6 pu | 0.6 pu |
| 0.5 pu | 0.5 pu | 0.5 pu | 0.6 pu | 0.5 pu | 0.6 pu | 0.5 pu | 0.5 pu | 0.5 pu | 0.6 pu |
| 0.5 pu | 0.5 pu | 0.5 pu | 0.5 pu | 0.5 pu | 0.5 pu | 0.4 pu | 0.5 pu | 0.5 pu | 0.5 pu |
| 0.4 pu | 0.4 pu | 0.4 pu | 0.4 pu | 0.4 pu | 0.4 pu | 0.4 pu | 0.4 pu | 0.4 pu | 0.4 pu |
| 0.4 pu | 0.4 pu | 0.4 pu | 0.4 pu | 0.4 pu | 0.4 pu | 0.4 pu | 0.4 pu | 0.4 pu | 0.4 pu |



Figure 10. The array output (before and after arrangement) for Case 2.

Table 6. $10 \times 10$ PV array parameters (before and after arrangement).

| Parameter | Before | After | Computing Time (s) | Power Improvement (Percentage) |
| :---: | :---: | :---: | :---: | :---: |
| $P_{m}$ | 2754 W | 3624 W |  |  |
| $V_{m}$ | 164 V | 171 V | 0.035 | $31.6 \%$ |
| $I_{m}$ | 16.95 A | 21.13 A |  |  |

### 4.4. Case 3: $100 \times 10$ PV Array

For a large PV array, say a $100 \times 10$ array with 100 parallel-connected strings and 10 seriesconnected modules. The non-uniform aging factors were randomly generated, as in Case 2 . It was observed that the power improved by $36.7 \%$ when the proposed algorithm was applied, and the average computing time was 2.746 s , as shown in Table 7.

Table 7. $100 \times 10 \mathrm{PV}$ array parameters (before and after arrangement).

| Parameter | Before | After | Computing Time (s) | Power Improvement (Percentage) |
| :---: | :---: | :---: | :---: | :---: |
| $P_{m}$ | 27.69 kW | 37.85 kW |  |  |
| $V_{m}$ | 165 V | 168 V | 2.746 | $36.7 \%$ |
| $I_{m}$ | 167 A | 223 A |  |  |

### 4.5. Discussions

From the results obtained for the three cases, it is observed that the proposed algorithm can be applicable to different sizes of PV arrays, and an improved maximum power has been achieved for the three cases. In addition, the proposed algorithm minimizes the effect of the bypass diodes by swapping the positions of the individual PV modules in each string based on their aging factors, so as to reduce the effect of mismatch losses amongst PV modules in a particular string, but the voltage limitation was not considered in this work, as discussed by the authors of [22]. This algorithm involves a hierarchical and iterative sorting of the PV modules. The $P-V$ curves (see Figures 9-11) for the three cases show that the effect of mismatch amongst the PV modules has been reduced after the rearrangement. Moreover, this proposed algorithm does not need to access all of the possible configurations for a particular PV array (huge number), which makes it relatively faster. For instance, in Case 1, only six steps were needed for the algorithm to arrive at the optimum arrangement (configuration) without having to go through all $2,627,625$ possible arrangements. The computing time for the three cases was recorded in Tables 4-7. In other words, the optimal configuration with the proposed algorithm can be found in a short time and can in turn be applied for implementation in real time. Interestingly, the affected PV modules to be swapped are the only ones involved in the transition, while the rest remain in their original position, therefore reducing the number of relays to be used for switching purposes, which in turn saves on cost and time compared with that proposed in refs. [6,22].


Figure 11. The array output (before and after arrangement) for Case 3.

## 5. Conclusions

In this work, we studied and analysed the non-uniform aging processes in the PV arrays, and we found that the locations of aged PV modules in the PV arrays influence the power generation of the solar PV arrays. In order to alleviate the negative influence of non-uniformly aging PV arrays, we propose a novel PV array reconfiguration algorithm to maximize the power generation from non-uniformly aged PV arrays, without replacing the aged PV modules with new ones. In this algorithm, the PV modules are sorted iteratively in a hierarchy pattern so as to reduce the effect of mismatch due to non-uniform aging amongst PV modules, and the maximum power output has improved by $22.4 \%, 31.6 \%$, and $36.7 \%$ for the $4 \times 4,10 \times 10$, and $100 \times 10$ arrays, respectively.

Compared to the existing online PV array reconfiguration methods, the proposed PV module rearrangement approach requires fewer relays but effectively boost the PV systems' maximum power output. The proposed algorithm is also easily implementable, as it only requires swapping locations of the PV modules.

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