A Maximum Throughput Channel Allocation Protocol in Multi-channel Multi-user Cognitive Radio Network

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Abstract: Channel allocation protocol plays a crucial role in ensuring that cognitive radio technology achieves more efficient radio resource utilization. To evaluate and compare the performance of different channel allocation protocols accurately, we need to model the cognitive radio system using various channel allocation protocols under one identical framework. Building such a framework is challenging. In this paper, we propose a method named distribution probability matrix (DPM) to describe the allocation results under different allocation protocols and a queueing analysis framework that uses DPM method. A framework that is capable of analyzing every individual secondary user (SU) in the system is developed. We also propose a maximum throughput (MT) channel allocation protocol aimed at achieving optimal throughput. Then we compare the MT protocol and existing protocols with the same objective using our proposed framework. The numerical results show that the MT protocol outperforms the existing protocols. Performance comparisons of different SUs are carried out to compare the protocols comprehensively. The convenience using our analytical framework of modeling different allocation protocols and evaluating the overall performances of the protocols is revealed throughout the analysis.

Index Terms: Cognitive radio, channel allocation protocol, maximum throughput, queueing analysis, performance evaluation.

I. Introduction

Cognitive radio (CR) technology is emerging as the most important solution to the growing conflict between the scarcity of spectrum resources and the proliferation of communication demands [1][2]. Different from the fixed spectrum assignment policy, CR technology enables the unlicensed user to utilize the temporarily unused licensed spectrum for promising applications, such as the smart grid, vehicular networks and emergency networks. The spectrum regulatory agencies (e.g., OFCOM in UK, FCC in the US, ICASA in South Africa) have attempted to open portions of the spectrum for opportunistic spectrum access. The development of the computing performances of devices also provides great potential for the effective use of radio frequency with CR technology. Thus the optimization of radio resource allocation using CR technology has become a pertinent topic to

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study [3], and in these research studies, performance evaluation and optimization problems are effectively analyzed and solved by queueing theory.

Queueing analysis of telecommunication systems [4] is well developed and has proven to be efficient. Some researchers make the queueing analysis applicable to cognitive radio networks (CRN). In [5], the data transmission process of a CR system can be described by a finite-state Markov process model. Then from a cross-layer perspective, the information transmitted in the CRN is modeled as a discrete form of packet, and a finite state Markov chain is used to describe the data transmission process using adaptive modulation and coding (AMC) in [6][7]. Performance measures such as average throughput and packet loss rate are derived. The performance of delay statistics can also be effectively analysed, as in [8], with queueing analysis.

Using similar queueing analysis, it can be investigated how different channel allocation protocols affect performance measures and how a channel allocation protocol should be designed in the CRN can be investigated. In [9], contention-based and contention-free medium access methods are used to allocate the channels. In [10], opportunistic spectrum scheduling is applied. To create a general framework to describe the channel allocation protocol and to evaluate the performance, the authors develop a distribution probability matrix (DPM) in [11].

Maximizing throughput is one of the most important objectives of channel allocation protocol in wireless communication systems [12][13]. In the CR system, there are many channel allocation protocols aimed at achieving optimal throughput for the secondary user (SU) system, such as opportunistic spectrum scheduling [10][14][15] and the maximum rate scheme [8]. In [16], the authors take a further step to take the primary user's activity into considerations. However the existing protocol has some limitations. First, the criterions of these channel allocation protocols are only based on the transmission capacity through primary user (PU) channels of each SU, regardless of the transmission demand of the SU. Second, most analysis is about one selected SU in the SU system and the behavior of other SUs is assumed to be independent and identical to the selected SU. The correlation among SUs is ignored and the performance evaluation of the allocation protocol is presented only by the selected SU rather than all SUs.

The contributions of this work are as follows: first, we introduce a new channel allocation protocol, named the maximum throughput (MT) protocol, to allocate multiple PU channels to multiple SUs. The target of the allocation protocol is to maximize the overall throughput of the SU system considering both

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Fig. 1. System model.

the SU transmission capacities and the transmission demands. Then analysis of each individual SU in the system is carried out by applying the MT protocol in a queueing analysis framework. Numerical results show that MT protocol yields better throughput than the opportunistic spectrum scheduling in [8][10][14]. Moreover, using our framework, various overall performance metrics of the SU system can be obtained precisely and efficiently because all the SUs are modeled independently. Thus the study of the factors that affect optimal throughput is also enabled by our framework. Investigations on the impact of channel quality and SU traffic density on the throughput and delay are carried out and the discoveries are indicated by the numerical results.

The rest of the paper is organized as follows. We introduce the system model and assumptions about our system in Section II. Then we show the algorithm of our MT allocation protocol and how the queueing model is built in Section III. Numerical results and the discussions on them are shown in Section IV. Section V concludes the paper.

II. The System Model and Assumptions

A. Basic Model Settings

In this research, we consider the uplink channel allocation for an overlay [17] CRN with multiple secondary users and multiple channels. A reliable channel between the PU and SU system that can help exchange control information is assumed. The CRN comprises one SU base station and multiple SUs working with it within the coverage of one or more PU networks. The number of SUs is denoted as M. The number of PU channels that are available to the SU system is denoted as J. All the data in the system are in the form of discrete packets and the SU system is working under the same time slot structure that is synchronized with the PU system. Each SU has a finite buffer to store the packets waiting to be transmitted. The buffer size of the *i*-th SU is denoted by K_i . An example of the system is shown in Fig. 1.

The channel allocation protocol is the rule that the SU system uses to decide at the beginning of a time slot which SU can transmit on the channel where the PU is absent. The objective of the channel allocation protocol in CRN is to satisfy the quality of service requirements, such as maximizing the throughput, minimizing the packet loss rate, and minimizing the packet delay.

B. Primary User Activity Model

The SU system cannot use a channel while there is a PU transmitting on it. The interference caused by the PU activities on a channel are modeled as a time-homogeneous first-order Markov process with two states that are independent from the SU system [18]. Let $O^{j}(t) \in \{0, 1\}$ represent the PU activity state of the *j*-th channel at the *t*-th time slot. When there is a PU transmitting, the state is called "busy" and $O^{j}(t) = 0$, otherwise the state is called "free" and $O^{j}(t) = 1$. The transition matrix of the process can be described as:

$$\boldsymbol{P}_{\mathrm{PU}}^{j} = \begin{pmatrix} p_{\mathrm{b}\to\mathrm{b}}^{j} & 1-p_{\mathrm{b}\to\mathrm{b}}^{j} \\ 1-p_{\mathrm{f}\to\mathrm{f}}^{j} & p_{\mathrm{f}\to\mathrm{f}}^{j} \end{pmatrix}, \tag{1}$$

where $p_{b\to b}^{j} = \Pr\{O^{j}(t) = 0 | O^{j}(t-1) = 0\}$ denotes the probability that there is a PU transmitting on the *j*-th channel (the "busy" state), given that there was a PU transmitting on the channel (the "busy" state) at the previous time slot, and $p_{f\to f}^{j} = \Pr\{O^{j}(t) = 1 | O^{j}(t-1) = 1\}$ denotes the probability that the *j*-th channel state changes from "free" to "free". From our model, average PU occupancy duration t_{PU} and percentage of PU occupancy θ can be obtained as follows:

$$t_{\rm PU} = \frac{1}{1 - p_{\rm b \to b}^j} \tag{2}$$

$$\theta = \frac{1 - p_{\mathrm{f} \to \mathrm{f}}^{j}}{2 - p_{\mathrm{b} \to \mathrm{b}}^{j} - p_{\mathrm{f} \to \mathrm{f}}^{j}}.$$
(3)

In our model, the PU occupancy state of all *J* channels at the *t*-th time slot is represented by *o*:

$$\boldsymbol{o} \in \mathcal{O} = \left\{ \left(O^1(t), O^2(t), \dots, O^J(t) \right) | O^j(t) \in \{0, 1\} \right\}.$$
(4)

C. Secondary System Transmission Model

The data transmission of the SU system is made under a time slot structure synchronized with the PU system. A time slot is divided into three consecutive parts, namely the spectrum sensing part, channel allocation part and data transmission part.

C.1 Spectrum Sensing

The SU system keeps sensing the existence of the PU transmission on every PU channel. In our model perfect spectrum sensing is assumed. At the end of the sensing part, the PU system estimates whether there is a PU transmitting on each channel and the transmission conditions (e.g: the signal-to-noise ratio at transceivers) of each SU mobile station.

C.2 Channel Allocation

According to the information the SU system collects in the spectrum sensing part, SU system allocates the available channels to the SUs for data transmission during the current time slot. In this paper, we assume that one PU channel can be allocated to only one SU. One SU can transmit through multiple PU channels. A flexible and configurable protocol DPM developed



Fig. 2. Illustration of DPM, time slot structure of SU system and the PU activity model.

by the authors in [11] is used. It works as follows: We set up a J-by-(M + 1) distribution probability matrix:

$$\boldsymbol{P}_{\text{DPM}} = \begin{pmatrix} d_0^1 & d_1^1 & \cdots & d_M^1 \\ \vdots & \vdots & \ddots & \vdots \\ d_0^J & d_1^J & \cdots & d_M^J \end{pmatrix}, \quad (5)$$

where d_0^j represents the probability that the *j*-th channel is not allocated to any SU, and d_i^j for $i \neq 0$ represents the probability that the SU system allocates the j-th channel to the i-th SU. According to our assumptions, in P_{DPM} : $\sum_{i=0}^{M} d_i^j = 1$ for any $j \in [1, J].$

C.3 Data Transmission

The channel condition is modeled as a Markov process. Perfect control information between the PU and SU system is assumed. AMC is used in the SU system. The SNR of the j-th PU channel at the SU receiver is divided into N_{SNR}^{j} states. Let $c^{j}(t) \in \{1, 2, \cdots, N_{\mathrm{SNR}}^{j}\}$ represent the channel condition state of the j-th channel at the t-th time slot. The evolution of the *j*-th channel condition states are modeled as a Markov chain using the same idea in [19]. The model is described by a $N_{\rm SNR}^{j}$ by- $N_{
m SNR}^j$ transition matrix $P_{
m CS}^j$. According to the modulation method, we can obtain the data transmission rate in every channel state. Details are shown in Appendix A.

As the error recovery method, a stop-and-wait automatic request and repeat (ARQ) is applied. The average packet error rate is Per.

After data transmission, all packets arriving during the time slot will enter the SU buffer. If the total number of packets exceeds the buffer size, the overflow packets will be rejected.

III. The Queueing Analysis and Channel Allocation Protocol

A. Queueing Model

A.1 Arrival Process

We assume that the number of arrival packets at each SU during one time slot follows a Batch Bernoulli process [20]. Let us denote the probability that j packets arrive during one time slot at the *i*-th SU by $\alpha_i(j)$. Hence we can use a probability vector α_i to describe the process as follows:

$$\boldsymbol{\alpha}_i = (\alpha_i(0), \alpha_i(1), \cdots, \alpha_i(v_i)), \tag{6}$$

where v_i is the maximum number of packets that can arrive at the *i*-th SU during one time slot and $\sum_{j=0}^{v_i} \alpha_i(j) = 1$.

A.2 State Space

The state of the system can be described by the number of packets in the buffer of each SU and the channel states. The number of packets in the buffer of each SU is denoted by:

$$\boldsymbol{b} \in \mathcal{B} = \left\{ (b_1, b_2, \cdots, b_M) | b_i \in \{0, 1, \cdots, K_i\}, i \in [1, M] \right\},$$
(7)

where b_i denotes the number of packets in the buffer of the *i*-th SU. The channel state is composed of all J channel condition states to all M SUs and the PU occupancy state of all J channels. Here we assume that the SNR of every PU channel is identical to one SU. Thus the channel condition state of J channels to M SUs is denoted by:

$$\boldsymbol{c} \in \mathcal{C} = \left\{ (c_1, c_2, \cdots, c_M) | c_i \in [1, N_{\text{SNR}}(i)] \right\}, \quad (8)$$

where c_i is the channel condition states of the PU channels, and $N_{\rm SNR}(i)$ is the number of channel condition states of the *i*-th SU. Thus together with the PU occupancy state in eq. (4), the channel state of the system can be described by $\{o, c\}$, and the number of channel states is $N_{\rm CS} = 2^J \cdot \prod_{i=1}^M N_{\rm SNR}(i)$. The state space of the system can be denoted by:

$$\Phi \triangleq \{ (\boldsymbol{b}, \boldsymbol{o}, \boldsymbol{c}) | \boldsymbol{b} \in \mathcal{B}, \boldsymbol{o} \in \mathcal{O}, \boldsymbol{c} \in \mathcal{C} \}.$$
(9)

The transition matrix between all the channel states $\{o, c\}$ is denoted by matrix **B** and **B** is obtained as:

$$\boldsymbol{B} = (\boldsymbol{P}_{\mathrm{PU}}^{1} \otimes \cdots \otimes \boldsymbol{P}_{\mathrm{PU}}^{J}) \otimes \boldsymbol{P}_{\mathrm{CS},1} \otimes \cdots \otimes \boldsymbol{P}_{\mathrm{CS},\mathrm{M}}, \quad (10)$$

where $oldsymbol{P}_{\mathrm{CS},\mathrm{i}}$ is the channel condition state transition probability matrix of the *i*-th SU in Appendix A and \otimes is the Kronecker product.

B. The Channel Allocation Protocol

B.1 Basic Ideas

Our maximum throughput protocol does not only focuses on the capacity of available PU channels, but also takes the transmission demands of SU into consideration. The channel is allocated to the SU that has the potential to transmit most packets in the following time slot among all SUs. The potential throughput of one SU is denoted by the minimum between the channel capacity according to the channel condition state and AMC (ignoring the effect of ARQ) and the transmission demands (number of packets in buffer). In order to minimize the probability

Algorithm 1 Algorithm for Block probability index index I_b **Input:** Arrival distribution: $\boldsymbol{\alpha} = \{\alpha_0, \alpha_1, \cdots, \alpha_v\}$ Service distribution: $\beta = \{\beta_0, \beta_1, \cdots, \beta_c\}$ Buffer size: K Number of packets in buffer: b **Output:** Block probability index: I_b for $i_1 = 0$ to K - 1 do 1: for $i_2 = 0$ to K - 1 do 2: for $i_3 = 0$ to v do 3: 4: for $i_4 = 0$ to c do if $i_2 = \min(v + i_1 - \min(i_1, c), K)$ then 5: Let $\Gamma_{i_3,i_4}(i_1,i_2) = 1$ 6: 7: Let $\Gamma_{i_3,i_4}(i_1,i_2) = 0$ 8: end if 9. end for 10: end for 11: end for 12: end for 13: 13: end for 14: Let matrix $\Gamma(i, i') = \begin{pmatrix} \Gamma_{0,0}(i, i') & \cdots & \Gamma_{v,0}(i, i') \\ \vdots & \ddots & \vdots \\ \Gamma_{0,c}(i, i') & \cdots & \Gamma_{v,c}(i, i') \end{pmatrix}$ 15: Let matrix $P = \begin{pmatrix} \alpha \Gamma(0, 0)\beta & \cdots & \alpha \Gamma(0, K-1)\beta \\ \vdots & \ddots & \vdots \\ \alpha \Gamma(K-1, 0)\beta & \cdots & \alpha \Gamma(K-1, K-1)\beta \end{pmatrix}$ 16: Let column vector $I_b = \begin{pmatrix} I_b(0) \\ I_b(1) \\ \vdots \\ I_b(K-1) \end{pmatrix}$ 17: Solve equation group: $(I = P)I_b = 1$ 18: end if 9: end for 10: Let $\mathcal{A} = \{A = (a_1, a_2, \cdots, a_J) | a_j \in a_j$ 11: Calculate $\mathcal{T} = \{\mathcal{T} = \text{Thr}(A) | A \in \mathcal{A}\}$. 12: Find the maximum in \mathcal{T} : T_{\max} . 13: Let $\mathcal{A}_{\max} = \{\mathcal{A}|\text{Thr}(A) = T_{\max}\}$. 14: for j = 1 to J do 15: Let L_j be the set of all values of $a_j, \forall A$. 16: Find the minimum in $I^j = \{I = I_b(l)\}$ 17: Solve equation group: $(I = P)I_b = 1$ 17: Solve equation group: (I - P)18: $I_b = I_b(b)$.

of rejecting arrival packet, when two or more SU have the same transmission potential, we will allocate the channel to the SU whose buffer is more likely to be full afterwards. We set up a block probability index (BPI) to describe how close the state of an SU's buffer is to the "full" state. Finally, the allocation results are represented in the form of DPM.

B.2 Calculation of the Potential Throughput

In our M-SU J-channel CRN, the allocation results are denoted by a vector $A = (a_1, a_2, \cdots, a_J)$, where $a_i = 0$ represents that the *j*-th channel is not allocated to the SU system and $a_i = i, i \in \{1, 2, \cdots, M\}$ represents that the *j*-th channel is allocated to the *i*-th SU. Given the channel condition state and the modulation schemes, we can obtain the capacity of the j-th channel to the *i*-th SU, which is denoted by cap_i^{J} . Thus we can obtain the total capacity of all the channels that are allocated to the *i*-th SU, which is denoted by cap_i as follows.

$$cap_i = \sum_{j \in \{j \mid a_j = i\}} cap_i^j. \tag{11}$$

The potential throughput can be denoted as Thr_i = $\min(b_i, cap_i)$, where b_i is the number of packets in the *i*-th SU's Algorithm 2 Algorithm for Channel Allocation Matrix D **Input:** Channel capacities to SU system: CNumber of packets in SU buffer: \mathcal{B} PU occupancy state: ONumber of SUs: M Number of PU channels: J **Output:** Distribution probability matrix: $\boldsymbol{D} = \begin{pmatrix} d_0^1 & d_1^1 & \cdots & d_M^1 \\ \vdots & \vdots & \ddots & \vdots \\ d_0^J & d_1^J & \cdots & d_M^J \end{pmatrix}$ Let $\boldsymbol{a}_j, 0 \le j \le J$ be a series of set representing the index 1: of the SUs that can access channel *j*. Set $d_i^j = 0$ for $i \in \{0, 1, \cdots, M\}, j \in \{1, 2, \cdots, J\}$ 2: 3: for j = 1 to J do if $O^j = 0$ then 4: $a_j = \{0\}, d_0^j = 1$ 5: 6: $a_i = \{1, 2, \cdots, M\}$ 7: end if 8: 10: Let $\mathcal{A} = \{A = (a_1, a_2, \cdots, a_J) | a_j \in a_j\}$ Let L_j be the set of all values of $a_j, \forall A \in \mathcal{A}_{\max}$. Find the minimum in $I^j = \{I = I_b(l) | l \in L_j\}$: I^j_{\min} Let $d^j = \{d | I_b(d) = I^j_{\min}\}$. N^j_d is the number of memfor all $i \in d^j$ do Let $d_i^j = \frac{1}{N_i^j}$ 19: end for 20: 21: end for

buffer. Finally we obtain the potential throughput of the given allocation result A as:

$$Thr(A) = \sum_{i=1}^{M} Thr_i.$$
 (12)

B.3 The Block Probability Index

The BPI is defined as the average first passage time from the current state to the full buffer state, ignoring the PU activity. We use a $\text{Geo}^{\text{X}}/\text{Geo}^{\text{Y}}/1/\text{K}$ queueing model to calculate the BPI. The inputs of the algorithm are the distribution of the number of arrival packets, the distribution of the number of packets that can be transmitted, the size of the buffer and the current number of packets in the buffer. The detail of the calculation is shown in Algorithm 1.

B.4 Algorithm of the Allocation Protocol

From the PU occupancy state: o, we can list all possible channel allocation results. Thus, given the capacities of PU

Table 1. Comparison of the runtime complexity of different channel allocation algorithms.

Protocols	Best case	Worst case
MT	$O(J \cdot \max(M^2, K^2 vc, K^4))$	$O(M \cdot J \cdot \max(M^J, K^2 vc, K^4))$
MR	$O(M^2 \cdot J)$	$O(M^{J+1} \cdot J)$
MT no Index	$O(M^2 \cdot J)$	$O(M^{J+1} \cdot J)$

to the SU system (
$$C = \begin{pmatrix} cap_1^{\rm L} & \cdots & cap_M^{\rm L} \\ \vdots & \ddots & \vdots \\ cap_1^{\rm L} & \cdots & cap_M^{\rm L} \end{pmatrix}$$
) and the

number of packets in the SUs' buffers, we can calculate the potential throughput of each allocation result. Let \mathcal{A}_{max} denote the set of allocation results that have the maximum potential throughput. Let L_i denote all the possible allocation results of the *i*-th channel in the set \mathcal{A}_{max} . Then calculate the BPI of all SUs in the set L_i . The distribution of the number of packets that each SU can transmit is calculated as described below. Assume that all the PU channels are utilized by the SU under the current SNR. Then using the probability of the channel condition state and the AMC transmission rate in Appendix A, the distribution can be obtained. Finally, the SUs in the set L_i that have the minimum BPI share the opportunity to access the *i*-th channel. Details are shown in Algorithm 2.

B.5 The Complexity Analysis of the Algorithm

channels

The calculation of the potential throughput and the BPI in the proposed MT algorithm increases the runtime complexity of the algorithm compared to the algorithms that ignore them. For comparison, a maximum rate (MR) protocol is implemented using our framework, as shown in Appendix B. The MR protocol is based on the principle that a PU channel is allocated to those SUs having the maximum transmission rate through that channel. A modified MT protocol (MT no Index) in which the BPI is removed is also proposed to provide a reference to investigate the trade-offs between the runtime complexity and the performance in the channel allocation algorithm design. The algorithm is shown in Appendix C. The details of the comparison of the complexity are listed in Table 1. The contribution of the the potential throughput and the BPI to the complexity is related to the SU buffer size K, the maximum arrivals v and the number of channel states c. The effect is significant when the number of SUs (M) and the number of PU channels (J) are small. When M and J become large, they will be the determining factors of the complexity of all algorithms. The complexity of the algorithms increases when the number of available PU channels increases. The best case is when there is only one PU channel available and the worst case is when all J channels are available. The runtime statistics and performance comparison are further discussed with numerical results in Section IV.

C. The Transition Matrix

To build the transition matrix, the distribution of the number of packets that can be transmitted needs to be derived first. According to the AMC scheme, we can obtain the distribution of transmitted packets of each SU through each channel given the channel condition state, c. The number of transmitted trials of the *i*-th SU through the *j*-th channel is denoted as C_i^j . According to the ARQ settings, we can obtain the probability that x packets can be transmitted as follows:

$$\mu(x) = \begin{cases} \binom{C_i^j}{x} (1 - \operatorname{Per})^x & \text{for } 0 \le x \le C_i^j \\ \operatorname{Per}^{C_i^j} & \text{for } x = 0 \end{cases}, \quad (13)$$

where $\binom{C_i^j}{x}$ is C_i^j choose x. Given the allocation results: $A = (a_1, \dots, a_J)$, we can obtain the distribution of the number of packets that can be transmitted by the *i*-th SU through the *j*-th channel:

$$\boldsymbol{\mu}_{i}^{j} = \begin{cases} \left(\boldsymbol{\mu}_{i}^{j}(0), \dots, \boldsymbol{\mu}_{i}^{j}(C_{i}^{j})\right) & \text{for } a_{j} = i\\ (0) & \text{otherwise} \end{cases}$$
(14)

Then the distribution of the number of packets that can be transmitted by the *i*-th SU given the channel state $\{o, c\}$ can be obtained as follows:

$$\boldsymbol{\mu}_i(\boldsymbol{A}, \boldsymbol{o}, \boldsymbol{c}) = \boldsymbol{\mu}_i^1 \ast \boldsymbol{\mu}_i^2 \ast \cdots \ast \boldsymbol{\mu}_i^J, \quad (15)$$

where * is the convolution. The transition matrix that describes that the number of packets in the *i*-th SU's buffer changes from b_i to b'_i under A:

$$\boldsymbol{P}(b_i, b'_i, \boldsymbol{A}) = \begin{bmatrix} \boldsymbol{\alpha}_i \boldsymbol{\Gamma}(0, 0) \boldsymbol{\mu}_i(\boldsymbol{A}) & \cdots & \boldsymbol{\alpha}_i \boldsymbol{\Gamma}(0, K) \boldsymbol{\mu}_i(\boldsymbol{A}) \\ \vdots & \ddots & \vdots \\ \boldsymbol{\alpha}_i \boldsymbol{\Gamma}(K, 0) \boldsymbol{\mu}_i(\boldsymbol{A}) & \cdots & \boldsymbol{\alpha}_i \boldsymbol{\Gamma}(K, K) \boldsymbol{\mu}_i(\boldsymbol{A}) \end{bmatrix}$$
(16)

where $\Gamma(i, i')$ is an auxiliary matrix, the elements of the matrix are derived as follows:

$$\Gamma_{v,c}(i,i') = \begin{cases} 1 & \text{when } i' = \min(v+i-\min(i,c),K) \\ 0 & \text{otherwise} \end{cases}$$
(17)

Let vector $\mathbf{b} = (b_1, b_2, \dots, b_M)$ denote the number of packets in each SU's buffer at the current time slot, and $\mathbf{b}' = (b'_1, b'_2, \dots, b'_M)$ denote the number of packets at the next time slot. Then we can get a coefficient matrix given \mathbf{b}, \mathbf{b}' and \mathbf{A} :

$$T(\boldsymbol{b}, \boldsymbol{b}', \boldsymbol{A}) = \boldsymbol{P}(b_1, b_1', \boldsymbol{A}) \otimes \boldsymbol{P}(b_2, b_2', \boldsymbol{A}) \otimes \cdots \otimes \boldsymbol{P}(b_M, b_M', \boldsymbol{A})$$
(18)

Using Algorithm 2, we can obtain a DPM D given a system state $\phi \in \Phi$, and using the elements in **D** we can obtain:

$$\boldsymbol{T}_{\boldsymbol{b},\boldsymbol{b}'}(\phi) = \sum_{\mathbf{A}\in\mathcal{A}} \left(\prod_{j=1}^{J} d_{a_j}^j \boldsymbol{T}(\boldsymbol{b},\boldsymbol{b}',\boldsymbol{A}) \right),$$
(19)

where d_i^j is from the DPM *D* and *A* is the set of all possible allocation results. We list all the coefficients according to the

index of channel condition states, and extend it into a matrix with the same dimensions as *B*:

$$\boldsymbol{T}_{\boldsymbol{b},\boldsymbol{b}'} = \begin{bmatrix} \boldsymbol{T}_{\boldsymbol{b},\boldsymbol{b}'}(1) \\ \vdots \\ \boldsymbol{T}_{\boldsymbol{b},\boldsymbol{b}'}(\phi) \\ \vdots \\ \boldsymbol{T}_{\boldsymbol{b},\boldsymbol{b}'}(N_{\mathrm{CS}}) \end{bmatrix} \otimes \boldsymbol{1}_{1 \times (\prod_{i=1}^{M} (K_i+1) \cdot N_{\mathrm{CS}})}.$$
(20)

To enumerate all coefficient matrices, we map β_{index} to $\boldsymbol{b} = (b_1, b_2, \cdots, b_M)$ using the following equation:

$$index = \sum_{i=1}^{M} b_i \times base_{i-1}, \qquad (21)$$

where $base_i = \prod_{j=i}^{M} (K_i + 1)$.

The transition matrix is divided into $\prod_{i=1}^{M} (K_i + 1) \times \prod_{i=1}^{M} (K_i + 1)$ blocks and obtained as follows:

$$\boldsymbol{P} = \begin{bmatrix} \boldsymbol{T}_{\beta_0,\beta_0} \circ \boldsymbol{B} & \dots & \boldsymbol{T}_{\beta_0,\beta_B} \circ \boldsymbol{B} \\ \vdots & \ddots & \vdots \\ \boldsymbol{T}_{\beta_B,\beta_0} \circ \boldsymbol{B} & \dots & \boldsymbol{T}_{\beta_B,\beta_B} \circ \boldsymbol{B} \end{bmatrix}, \quad (22)$$

where \circ is the entrywise product.

D. Performance Evaluation

We can apply the QR algorithm to get the steady probability vector: π' given the transition matrix P. π' is a 1 by $\prod_{i=1}^{M} (K_i + 1) \times N_{\text{CS}}$ vector. According to the sequences of the states in P, we can map a vector $(\boldsymbol{b}, \boldsymbol{o}, \boldsymbol{c})$ to any system state. The steady probability vector is reorganized in the form of $\pi(\boldsymbol{b}, \boldsymbol{o}, \boldsymbol{c})$.

D.1 Queue Length Distribution

Since we have obtained the steady probability vector of each system state, the distribution of the number of packets in the *i*-SU's buffer can be derived. Let $P(q_i = l)$ denote the probability that there are l packets waiting to be transmitted in the *i*-th SU's buffer; this can be calculated as follows:

$$P(q_i = l) = \sum_{\boldsymbol{b} \in B_i(l)} \boldsymbol{\pi}(\boldsymbol{b}, \boldsymbol{o}, \boldsymbol{c}),$$
(23)

where $B_i(l) = \{(b_1, \dots, b_M) | b_i = l, b_j \in [0, K_j], j \neq i\}$, which represents the set of the state of SU buffers when the *i*-th SU has *l* packets in the buffer. The average queue length of the *i*-th SU can be obtained as:

$$\overline{q_i} = \sum_{l=1}^{K_i} l \cdot P(q_i = l).$$
(24)

D.2 Average Throughput

The average throughput is described by the expected number of packets that can be transmitted during one time slot. First, we can calculate the distribution of the transmission the *i*-th SU can make under each system state $\phi = (\mathbf{b}, \mathbf{o}, \mathbf{c}) \in \Phi$, which is denoted by $\mathbf{t}_i(\mathbf{b}, \mathbf{o}, \mathbf{c})$ as follows:

$$\boldsymbol{t}_{i}(\boldsymbol{b},\boldsymbol{o},\boldsymbol{c}) = \sum_{A \in \mathcal{A}^{\dagger}(\boldsymbol{\gamma})} d_{i}^{j} \boldsymbol{\mu}_{i}(A,\boldsymbol{o},\boldsymbol{c})$$
(25)

where d_i^j is from the DPM **D** obtained from ϕ and $\mathcal{A}^{|}(\rangle)$ is the set of allocation results when the *j*-th channel is allocated to the *i*-th SU. Then we can obtain the average throughput under $(\boldsymbol{b}, \boldsymbol{o}, \boldsymbol{c})$:

$$Thr(b, o, c) = \sum_{\beta} \min(\beta, b_i) \cdot t_i(b, o, c, \beta), \qquad (26)$$

where $t_i(\mathbf{b}, \mathbf{o}, \mathbf{c}, \beta)$ is the $(\beta+1)$ -th element of $t_i(\mathbf{b}, \mathbf{o}, \mathbf{c})$, which is the probability that β transmissions have been made by the *i*-th SU under $(\mathbf{b}, \mathbf{o}, \mathbf{c})$. Thus the average throughput can be derived as:

$$\overline{\mathrm{Thr}} = \sum_{\phi \in \Phi} \mathrm{Thr}(\mathbf{b}, \mathbf{o}, \mathbf{c}) \cdot \boldsymbol{\pi}(\mathbf{b}, \mathbf{o}, \mathbf{c}).$$
(27)

D.3 Average Rejection

The average rejection is the expected number of packets that are rejected at the beginning of one time slot. First we can calculate the average number of rejected under (b, o, c):

$$\operatorname{Rej}(\mathbf{b}, \mathbf{o}, \mathbf{c}) = \sum_{\mathbf{y}=0}^{\mathbf{v}_{i}} \sum_{\beta} \max(0, b_{i} - \beta + y - K_{i}) \\ \cdot \min(\beta, b_{i}) \cdot t_{i}(\mathbf{b}, \mathbf{o}, \mathbf{c}, \beta).$$
(28)

Thus the average number of rejected packets can be derived as:

$$\overline{\text{Rej}} = \sum_{\phi \in \Phi} \text{Rej}(b, o, c) \cdot \boldsymbol{\pi}(b, o, c).$$
(29)

D.4 Average Delay

We use the expected time to transmit all packets that are in the buffer at the beginning of a time slot to describe the delay performance. To calculate it, the state space is extended with another state f, which is the number of packets that have already been transmitted by a selected SU. The extended system state is $\Phi' \triangleq \{b, o, c, f\}$. Then we need to modify the transition matrix based on P given that there are F packets in the selected SU's buffer. First, we build an extending band matrix given $\{b, o, c\}$ and t_i obtained as in eq. (25):

$$\boldsymbol{P}_{\mathrm{ex}}(\boldsymbol{b},\boldsymbol{o},\boldsymbol{c},f) = \begin{bmatrix} P_{0,0} & \cdots & P_{0,f} \\ \vdots & \ddots & \vdots \\ P_{f,0} & \cdots & P_{f,f} \end{bmatrix}, \quad (30)$$

where

$$P_{j,j'} = \begin{cases} t_i(\boldsymbol{b}, \boldsymbol{o}, \boldsymbol{c}, j' - j) & \text{for } j' - j \in [0, \max(\boldsymbol{t}_i(\boldsymbol{b}, \boldsymbol{o}, \boldsymbol{c}))] \\ 0 & \text{otherwise} \end{cases}$$

where $\max(t_i(b, o, c))$ is the maximum number of packets that can be transmitted under (b, o, c). Then extend P row by row as follows:

$$\boldsymbol{P}_{\text{delay}}^{\prime}(f) = \begin{bmatrix} \boldsymbol{P}(\phi(0)) \otimes \boldsymbol{P}_{\text{ex}}(\phi(0), f) \\ \vdots \\ \boldsymbol{P}(\phi(R_m)) \otimes \boldsymbol{P}_{\text{ex}}(\phi(R_m), f) \end{bmatrix}$$
(31)



Fig. 3. Throughput versus SNR under MT protocol and MR protocol.

where $R_m = \prod_{i=1}^M (K_i + 1) \cdot N_{\text{CS}}$ is the size of \boldsymbol{P} , and $P(\phi(r))$ is the *r*-th row of matrix \boldsymbol{P} . The absorbing states are when all the packets in the buffer are transmitted ($b_i = 0$ or f = F), then remove them from $\boldsymbol{P}'_{\text{delay}}$ to get $\boldsymbol{P}_{\text{delay}}$. The average first passage time to the absorbing states under each state is denoted as \boldsymbol{S} , which is a $(K_s \cdot \prod_{i \neq s} (K_i + 1) \cdot N_{\text{CS}})$ -by-1 column vector (s is the label of the selected SU). Then solve \boldsymbol{S} from the following equations:

$$(\boldsymbol{I} - \boldsymbol{P}_{\text{delay}}) \cdot \boldsymbol{S} = \boldsymbol{1} \tag{32}$$

where I is the identity matrix, 1 is a column vector whose elements are all 1 and size is the same as S. Let S(F) be the solution of S given F. Thus the average first passage time from the state of F packets in the buffer to the empty buffer state is:

$$d(F) = \sum_{\boldsymbol{b} \in B_s(F)} S(F, \boldsymbol{b}, \boldsymbol{o}, \boldsymbol{c}) \cdot \boldsymbol{\pi}(\boldsymbol{b}, \boldsymbol{o}, \boldsymbol{c}), \qquad (33)$$

where $B_s(F)$ is the set of all states of SU buffers when the selected SU's buffer has F packets in it. Finally the average delay of the *i*-th SU can be obtained as:

$$\overline{\text{Dly}} = \sum_{t=1}^{K_i} d(t).$$
(34)

IV. Numerical Results and Discussion

To control complexity without loss of generality, we implement a model with two users and two channels to show the performance of our protocol and the advantages of our analytical framework. The default settings are as follows:

- Channel condition: $P_{target} = 0.01, f_m = 10 \, \text{Hz}$
- PU activity model: $t_{PU} = 1, \theta = 50\%$
- Arrival process: $\alpha_1 = \alpha_2 = \{0.5, 0.5\}$
- SU buffer size: $K_1 = 6, K_2 = 6$
- ARQ setting: Per = 0.01

A. Performance Comparison

In this part, we first compare the throughput of our protocol with the MR protocol under different SNRs. Fig. 3 shows the throughputs of the MT and MR protocols under different



Fig. 4. Queue length distribution under MT protocol and MR protocol.



Fig. 5. Throughput improvements versus different system settings.

SNRs. Using our protocol, there is 10% to 20% improvement on throughput under the given conditions. The demand of buffer size can also be improved by our protocol. Fig. 4 shows the queue length distributions under MT and MR protocols versus different SNRs. Then we change the PU activity parameters and SU arrival process parameters to investigate how the other system parameters affect the throughput improvement. The results are shown in Fig. 5. In Fig. 5(a), θ is the PU occupancy probability of both channels; in Fig. 5(b), α is the probability that no packets arrived during the previous time slot. We can observe from the results that our protocol will achieve greater improvement when there is more PU activity or there are more SU arrival



Fig. 6. Throughput of SU 1 and SU 2 versus SNRs.

packets. We can conclude that our protocol can outperform the MR protocol on optimizing throughput and it is able to achieve more improvement when the SU system has more spectrum resources or the traffic of the SU system is heavier.

B. Performance Analysis of Multiple SUs

One advantage of our analytical framework is that all PU channels can be set independently, and the performance of all SUs can be obtained simultaneously. We investigate the influence of channel allocation protocol on both SUs when the system parameters vary. Fig. 6 shows the throughput of SU 1 and SU 2 when the SNR of the two SUs vary. We choose an SNR setting to compare the performance between the MT and MR protocols in terms of the throughput of the two SUs. The results are shown in Fig. 7. We can observe a shortcoming of the MR protocol. When one of the SUs has a better transmission condition (higher SNR), the MR protocol inclines to allocate the channels to it even if it does not have enough transmission demands (packets waiting in the buffer), which will obviously waste the precious spectrum resources. Moreover, the increase of the SNR of the SU under worse transmission conditions does not affect the performance of the SU under better conditions. In Fig. 8, the average delay of SU 1 and SU 2 under different SNRs is shown and we find that the delay performance of the SU with better SNR remains the same while the other SU's SNR increases. Using our protocol, the increase of one SU's SNR does not affect another SU's transmission seriously when there is sufficient spectrum resource. This proves that our MT protocol can guarantee the transmission quality of each SU in the system, thus achieving higher resource utilization efficiency besides improving throughput.

The analyses above reveal the advantages of our framework. First, our framework is capable of modeling various channel allocation protocols and comparing their performance in different situations. Second, using our framework, the individual performances of the SUs can be obtained simultaneously and from these results we are able to investigate how a channel allocation protocol works in more detail.



Fig. 7. Throughput of 2 SUs versus SNR of SU 1. The SNR of SU 2 is 10 dB.



Fig. 8. Average delay of SU versus SNR. The SNR of SU 2 is 30 dB. α is the probability that no packets arrived at SU 1 during the previous time slot.



Fig. 9. Packet rejection rate versus SNR under different protocols.

C. Discussion on Trade-offs between Performance and Complexity

In this part, the performance and runtime complexity of the MT, MR and MT without BPI are compared through numerical



Fig. 10. Throughput versus SNR under different protocols.

results to illustrate the trade-offs in channel allocation protocol design. Because BPI is designed to improve the packet rejection rate, the rejection rate together with the throughput under different SNR settings are compared in Fig. 9 and Fig. 10. The results reveal that the potential throughput part provides most of the improvement on throughput and rejection rate and BPI can be applied to achieve utmost performance improvement. The codes of the three protocols are run in MATLAB 1000 times in an identical hardware environment; the runtime statistics are shown in Fig. 11. The potential throughput and the BPI part of the MT protocol are the reason it outperforms the MR. However, the trade-offs between the computing resource consumption and performance improvement should be considered in practice. For example, the BPI part may not be a good choice if the computing resources of the SU system are limited because this part consumes great computing resources but does not yield much improvement. This part of the analysis shows that our framework is able to reveal the trade-offs in channel allocation protocol design from some specific angles and provide a reference when designing and evaluating CR channel allocation protocols.

V. Conclusions

In this paper, we introduce an improved channel allocation protocol named the maximum throughput protocol. Compared to previous channel allocation protocols with similar ideas, such as MR, it can improve the SU system performance of throughput by 10% to 20% under the selected system settings. Performance measures such as the queue length in the buffer and the packet rejection rate are also improved. The negative effects of SUs under poor transmission conditions in the system on the performance of one SU, such as throughput and packet delay, can also be reduced by our protocol.

To carry out the analysis of different channel allocation protocols, we establish a queueing model using the concept of the DPM proposed by the authors and it proves to be practicable to describe channel allocation protocols in queueing analysis frameworks. Our framework is able to describe the behavior of each individual SU in the system independently and to evaluate various performance measures of each SU under various system and environment settings. This makes our framework capable



Fig. 11. Runtime statistics of different channel allocation protocols (1000 times).

of comparing different channel allocation protocols from more angles and in more detail. The ideas presented in this paper can also provide convenience in channel allocation design and performance optimization for CRN.

APPENDICES

I. Channel state model

To make better use of the limited channel resources, AMC scheme is applied by the SU system [21]. In this paper, the convolutionally coded M_n -ary rectangular and square QAM modes are used as the modulation method, which are adopted from the HIPERLAN/2 and IEEE 802.11a standards [22]. The details of the AMC scheme are listed in Table 2.

According to the SNR at the SU's transceiver, we divide the j-th channel condition into $N_{\rm SNR}^{j}$ states. Different modulation schemes are used to make the average packet error rates under each state all equal to a required packet error rate $P_{\rm target}$. Thus, the boundary of these states in terms of SNR can be written in [6] as:

$$\Gamma_n = \frac{1}{g_n} \ln \left(\frac{a_n}{P_{\text{target}}} \right), \tag{35}$$

where $\Gamma_n \in {\Gamma_0, \Gamma_1, \ldots, \Gamma_{N_{\text{SNR}}^j}}$ denotes the boundary SNR of each state. Then by setting $\Gamma_0 = 0$ and $\Gamma_{N_{\text{SNR}}^j+1} = \infty$, we can obtain the $N_{\text{SNR}}^j + 1$ SNR boundary of the N_{SNR}^j states. When the SNR at an SU's receiver satisfies $\gamma \in (\Gamma_0, \Gamma_1)$, the SU does not transmit any packets because of the low SNR and when $\gamma \in (\Gamma_n, \Gamma_{n+1}), n \in \{1, 2, \ldots, N_{\text{SNR}}^j - 1\}$, the SU is transmitting using mode n.

Then we can determine how the channel condition state evolves. We assume that the channel is Rayleigh: the SNR is exponentially distributed with the probability density function:

$$p(\gamma_i^j) = \frac{1}{\bar{\gamma}_i^j} \exp\left(-\frac{\gamma_i^j}{\bar{\gamma}_i^j}\right), \quad \gamma_i^j \ge 0$$
(36)

where γ_i^j is the received SNR of the *i*-th SU on the *j*-th channel,

	Mode 1	Mode 2	Mode 3
Modulation	BPSK	QPSK	8-QAM
Coding rate R_c	1/2	1/2	3/4
Rate(bits/symbol)	0.5	1	1.5
a_n	274.7229	90.2514	67.6181
g_n	7.9932	3.4998	1.6883
$\Gamma_n(dB)$	-1.5331	1.0942	3.9722

Table 2. Transmission modes with convolutionally coded modulation

and $\bar{\gamma}_i^j$ is the average SNR. The steady state probability of the *k*-th state of the *i*-th SU on the *j*-th channel can be obtained by:

$$\pi_{\mathrm{CS},i}^{j}(k) = \int_{\Gamma_{k}}^{\Gamma_{k+1}} p(\gamma_{i}^{j}) \,\mathrm{dx}.$$
(37)

 $\begin{array}{l} \mathrm{Let} \ \boldsymbol{P}_{\mathrm{CS},\mathrm{i}}^{j} = \begin{pmatrix} p_{0,0} & \cdots & p_{0,N_{\mathrm{SNR}}^{j}-1} \\ \vdots & \ddots & \vdots \\ p_{N_{\mathrm{SNR}}^{j}-1,0} & \cdots & p_{N_{\mathrm{SNR}}^{j}-1,N_{\mathrm{SNR}}^{j}-1} \end{pmatrix} \mathrm{denote} \ \mathrm{the} \\ \mathrm{state} \ \mathrm{transition} \ \mathrm{probability} \ \mathrm{matrix} \ \mathrm{of} \ \mathrm{the} \ i\text{-th} \ \mathrm{SU} \ \mathrm{on} \ \mathrm{the} \ j\text{-th} \end{array}$

state transition probability matrix of the *i*-th SU on the *j*-th channel, where $p_{a,b}$ denotes the transition probability from state *a* to state *b*. According to [23], the transition probabilities can be obtained by:

$$p_{k,k+1} \approx \frac{f_m T_p}{\pi_{\text{CS},i}^j(k)} \sqrt{\frac{2\pi\Gamma_{k+1}}{\bar{\gamma}_i^j}} \exp\left(-\frac{\Gamma_{k+1}}{\bar{\gamma}_i^j}\right), \qquad (38)$$

$$p_{k,k-1} \approx \frac{f_m T_p}{\pi_{\text{CS},i}^j(k)} \sqrt{\frac{2\pi\Gamma_k}{\bar{\gamma}_i^j}} \exp\left(-\frac{\Gamma_k}{\bar{\gamma}_i^j}\right), \qquad (39)$$

where f_m is the maximum Doppler frequency. We assume that the transition occurs only between adjacent states: $p_{j,k} = 0$, for any |j-k| > 1, and $p_{k,k} = 1 - p_{k,k+1} - p_{k,k-1}$, then with (35)-(39), the transition probability matrix P_{CS}^j can be obtained. Let $V_{\text{rate}}(k)$ represent the number of packets that can be transmitted in one time slot when the channel condition state is k. According to the rate in Table 2, $V_{\text{rate}}(k)$, $k \in \{0, 1, 2, 3\} = \{0, 1, 2, 3\}$.

II. Algorithm for Maximum Rate Protocol

The calculation of function Thr_{max} is similar to the potential throughput in eq. (12). Given the channel condition state and the modulation schemes, we can obtain the total capacity of all the channels that are allocated to the *i*-th SU, which is denoted by cap_i . The throughput of the given allocation result A under MR is as:

$$Thr_{MR}(A) = \sum_{i=1}^{M} cap_i.$$
 (40)

III. Algorithm for Maximum Throughput Protocol without BPI

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Maximum Rate Maximum Rate Input: Channel capacities to SU system: C Number of packets in SU buffer: \mathcal{B} PU occupancy state: o Number of SUs: M Number of PU channels: J Output: Distribution probability matrix: $D = \begin{pmatrix} d_0^1 & d_1^1 & \cdots & d_M^1 \\ \vdots & \vdots & \ddots & \vdots \\ d_0^J & d_1^J & \cdots & d_M^J \end{pmatrix}$ 1: Let $a_i, 0 \le j \le J$ be a series of set representing the index
Input: Channel capacities to SU system: C Number of packets in SU buffer: \mathcal{B} PU occupancy state: o Number of SUs: M Number of PU channels: J Output: Distribution probability matrix: $D = \begin{pmatrix} d_0^1 & d_1^1 & \cdots & d_M^1 \\ \vdots & \vdots & \ddots & \vdots \\ d_0^J & d_1^J & \cdots & d_M^J \end{pmatrix}$ 1: Let $a_i, 0 \le j \le J$ be a series of set representing the index
Number of packets in SU buffer: \mathcal{B} PU occupancy state: o Number of SUs: M Number of PU channels: J Output: Distribution probability matrix: $D = \begin{pmatrix} d_0^1 & d_1^1 & \cdots & d_M^1 \\ \vdots & \vdots & \ddots & \vdots \\ d_0^J & d_1^J & \cdots & d_M^J \end{pmatrix}$ 1: Let $a_i, 0 \le j \le J$ be a series of set representing the index
PU occupancy state: o Number of SUs: M Number of PU channels: J Output: Distribution probability matrix: $D = \begin{pmatrix} d_0^1 & d_1^1 & \cdots & d_M^1 \\ \vdots & \vdots & \ddots & \vdots \\ d_0^J & d_1^J & \cdots & d_M^J \end{pmatrix}$ 1: Let $a_i, 0 \le j \le J$ be a series of set representing the index
Number of SUs: <i>M</i> Number of PU channels: <i>J</i> Output: Distribution probability matrix: $D = \begin{pmatrix} d_0^1 & d_1^1 & \cdots & d_M^1 \\ \vdots & \vdots & \ddots & \vdots \\ d_0^J & d_1^J & \cdots & d_M^J \end{pmatrix}$ 1: Let $a_i, 0 \le j \le J$ be a series of set representing the index
Number of PU channels: J Output: Distribution probability matrix: $D = \begin{pmatrix} d_0^1 & d_1^1 & \cdots & d_M^1 \\ \vdots & \vdots & \ddots & \vdots \\ d_0^J & d_1^J & \cdots & d_M^J \end{pmatrix}$ 1: Let $a_i, 0 \le j \le J$ be a series of set representing the index
Output: Distribution probability matrix: $D = \begin{pmatrix} d_0^1 & d_1^1 & \cdots & d_M^1 \\ \vdots & \vdots & \ddots & \vdots \\ d_0^J & d_1^J & \cdots & d_M^J \end{pmatrix}$ 1: Let $a_i, 0 \le j \le J$ be a series of set representing the index
$D = \begin{pmatrix} d_0^1 & d_1^1 & \cdots & d_M^1 \\ \vdots & \vdots & \ddots & \vdots \\ d_0^J & d_1^J & \cdots & d_M^J \end{pmatrix}$ 1: Let $a_i, 0 \le j \le J$ be a series of set representing the index
$D = \begin{pmatrix} a_0 & a_1 & \cdots & a_M \\ \vdots & \vdots & \ddots & \vdots \\ d_0^J & d_1^J & \cdots & d_M^J \end{pmatrix}$ 1: Let $a_i, 0 \le j \le J$ be a series of set representing the index
$D = \begin{pmatrix} \vdots & \vdots & \ddots & \vdots \\ d_0^J & d_1^J & \cdots & d_M^J \end{pmatrix}$ 1: Let $a_i, 0 < j < J$ be a series of set representing the index
1: Let $a_i, 0 \le j \le J$ be a series of set representing the index
1: Let $a_i, 0 \le j \le J$ be a series of set representing the index
J' = J = I = C
of SU that can access channel <i>j</i> .
2: Set $d_i^j = 0$ for $i \in \{0, 1, \cdots, M\}, j \in \{1, 2, \cdots, J\}$
3: for $j = 1$ to J do
4: if $O^j = 0$ then
5: $a_j = \{0\}, d_0^j = 1$
6: else
7: $a_j = \{1, 2, \cdots, J\}$
8: end if
9: end for
10: Let $\mathcal{A} = \{\mathcal{A} = (\dashv_{\infty}, \dashv_{\in}, \cdots, \dashv_{\mathcal{J}}) \dashv_{ } \in \dashv_{ }\}$
11: Calculate $\mathcal{T} = \{\mathcal{T} = \operatorname{Thr}_{\operatorname{MR}}(A) A \in \mathcal{A}\}.$
12: Find the maximum in \mathcal{T} : T_{max} .
13: Let $\mathcal{A}_{\max} = \{\mathcal{A} \operatorname{Thr}_{\operatorname{MR}} (A) = T_{\max} \}.$
14: for $j = 1$ to J do
15: Let L_j be the set of all values of $a_j, \forall A \in \mathcal{A}_{\max}$.
16: N_d^j is the number of members in L_j .
17: for all $i \in L_j$ do
18: Let $d_i^j = \frac{1}{N^j}$
19: end for
20: end for

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Algorithm 4 Algorithm for Channel Allocation Matrix D of Maximum Throughput without BPI

Input: Channel capacities to SU system: CNumber of packets in SU buffer: \mathcal{B} PU occupancy state: o Number of SUs: M Number of PU channels: J **Output:** Distribution probability matrix: $D = \begin{pmatrix} d_0^1 & d_1^1 & \cdots & d_M^1 \\ \vdots & \vdots & \ddots & \vdots \\ d_0^J & d_1^J & \cdots & d_M^J \end{pmatrix}$ Let $a_j, 0 \le j \le J$ be a series of set representing the index 1: of SU that can access channel *j*. Set $d_i^j = 0$ for $i \in \{0, 1, \cdots, M\}, j \in \{1, 2, \cdots, J\}$ 2: for j = 1 to J do 3: if $O^j = 0$ then 4: $a_j = \{0\}, d_0^j = 1$ 5: else 6: $a_j = \{1, 2, \cdots, J\}$ 7: 8: end if 9: end for 10: Let $\mathcal{A} = \{\mathcal{A} = (\dashv_{\infty}, \dashv_{\in}, \cdots, \dashv_{\mathcal{J}}) | \dashv_{I} \in \dashv_{I}\}$ 11: Calculate $\mathcal{T} = \{\mathcal{T} = \text{Thr}(A) | A \in \mathcal{A}\}.$ 12: Find the maximum in \mathcal{T} : T_{max} . Let $\mathcal{A}_{\max} = \{\mathcal{A} | \operatorname{Thr} (A) = T_{\max} \}.$ 13: for j = 1 to J do 14: Let L_j be the set of all values of $a_j, \forall A \in \mathcal{A}_{\max}$. 15: N_d^j is the number of members in L^j . 16: for all $i \in L^j$ do Let $d_i^j = \frac{1}{N_d^j}$ 17: 18: 19: end for

20: **end for**

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